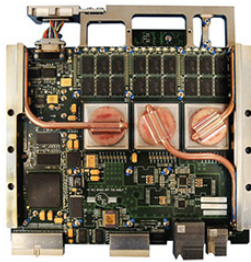




RCC 5



SEAKR[®]'s fifth generation ReConfigurable Computer (RCC[®])

The RCC5 is designed for today's demanding space payload processing applications. It is an extended 6u cPCI module with PCI, GPIO, and SERDES interconnect.

For more information, please contact proposal.team@seakr.com.

RCC 5 Technical Specifications

System Overview

- Three Virtex-5 FX-130T FPGAs
- On board configuration storage with optional scrubber
- High speed SERDES I/O
- Conduction Cooled
- TRL9

Memory

- 3 GByte DDRII SDRAM
- 16 Gbits of configuration memory

Backplane I/O

- 32b/33 MHz PCI 2.2 Compliant
- 12 Multi Gigabit Transceiver (MGT) SERDES
- 4 Multi Gigabit Transceiver (MGT) SERDES from the mezzanine routed to the backplane

Mezzanine I/O

- 24 Multi Gigabit Transceiver (MGT) SERDES
- 339 GPIO

SWaP

- Extended 6u Compact PCI
- 3.5 lbs
- Power consumption is a function of the FPGA code utilization