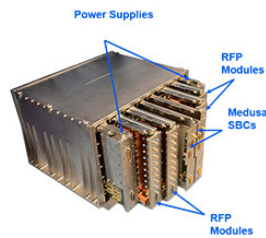




HDP



## SEAKR<sup>®</sup>'s latest Hopping Dehopping Processor

The HDP is single point failure immune, and contains two power supply modules (PSM), each with a ~400 W per supply.

For more information, please contact [proposal.team@seakr.com](mailto:proposal.team@seakr.com).

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### HDP Technical Specifications

#### System Overview

- 4 RCC nodes
- 2 Power supplies
- 2 Athena SBC
  - Provides command/telemetry and application software

#### 4 RF Processor (RFP) Modules

- (4) Xilinx Virtex 5 FPGA processor/Module
- (4) RF inputs/outputs per module
- 1200MHz clock inputs
- Scalable to 8 RFP modules

#### Applications

- Beamforming
- Channelization
- MODEM
- RF signal processing
- specialized comms